

Fig.1 (Prior Art)

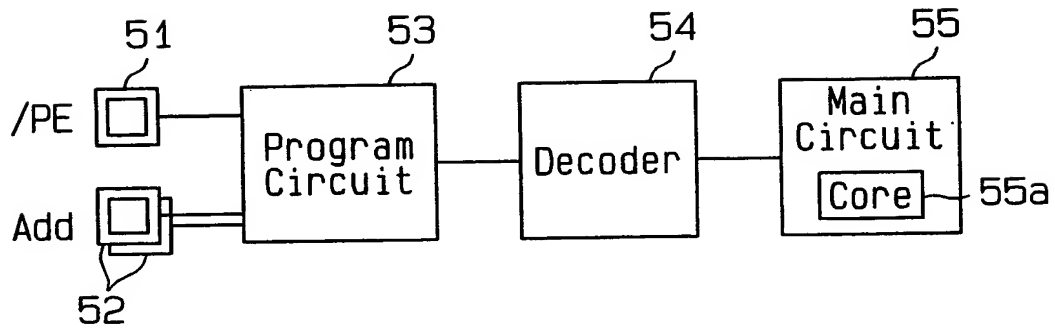


Fig.2 (Prior Art)

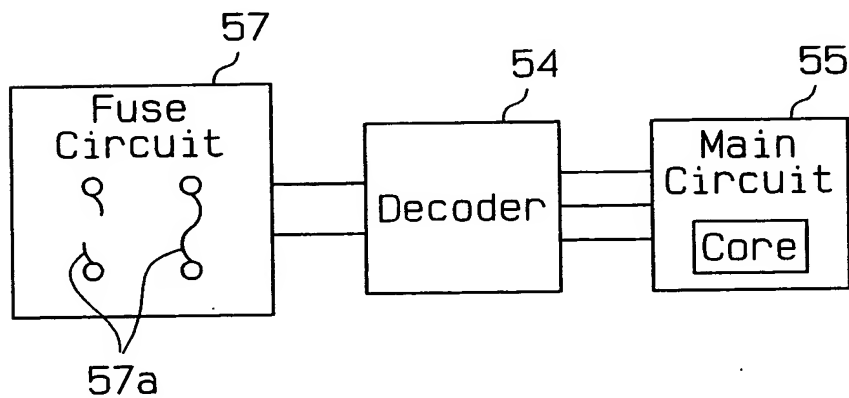


Fig.3

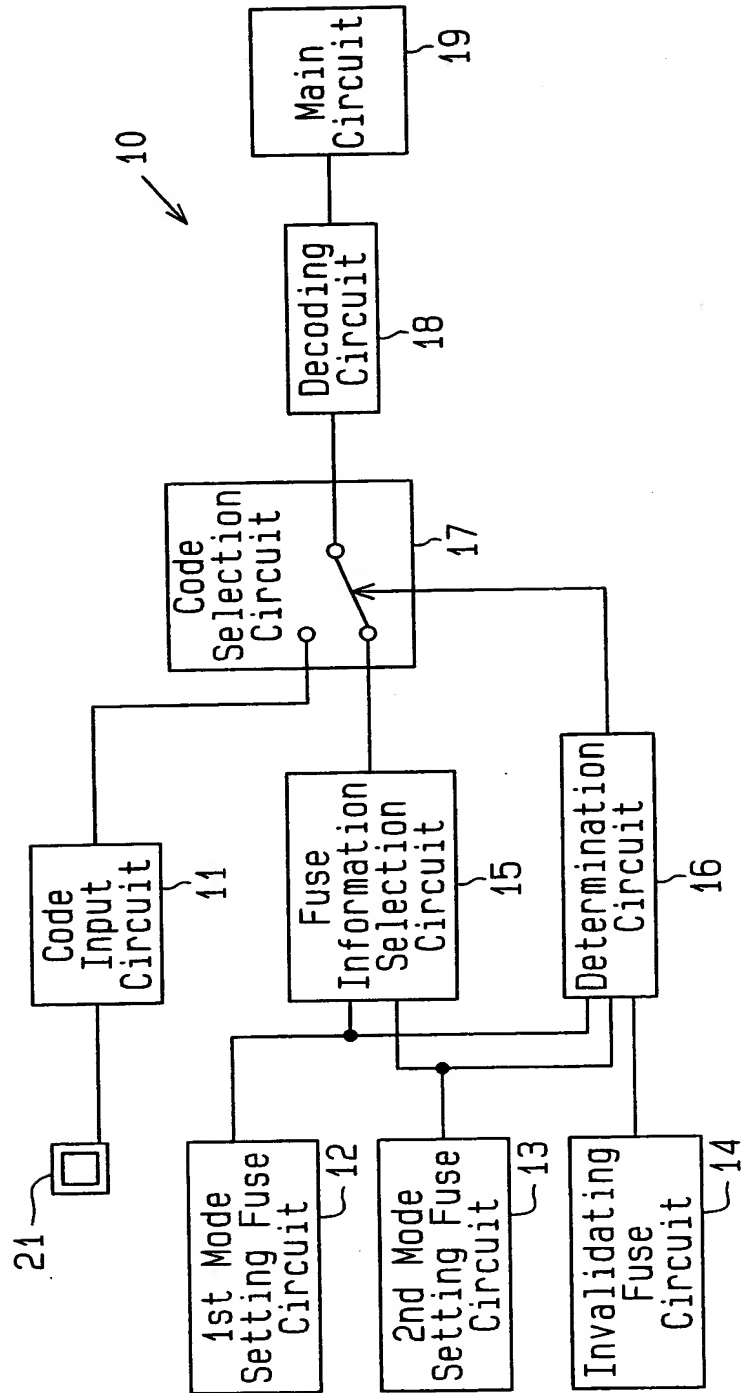


Fig.4

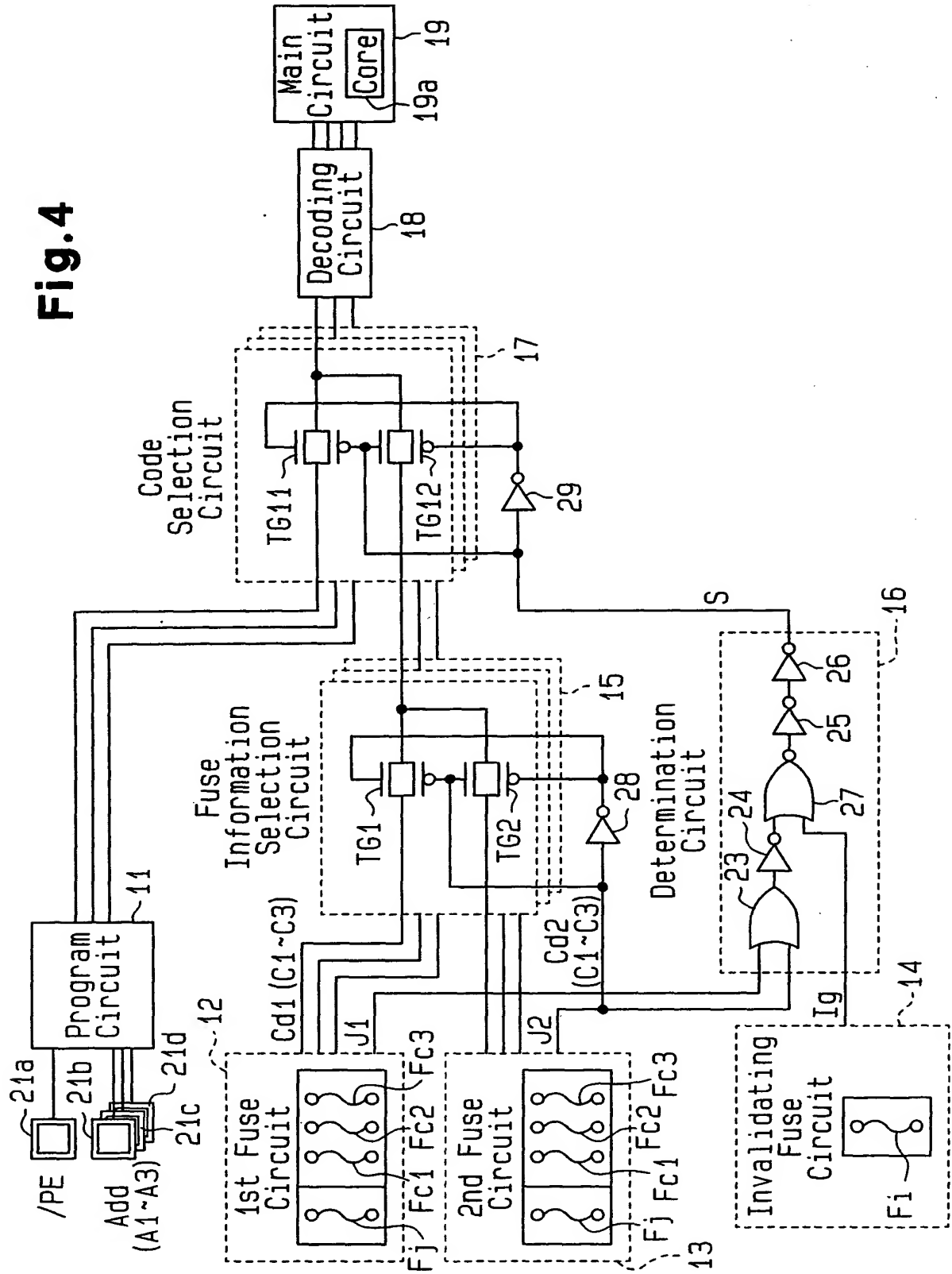


Fig.5

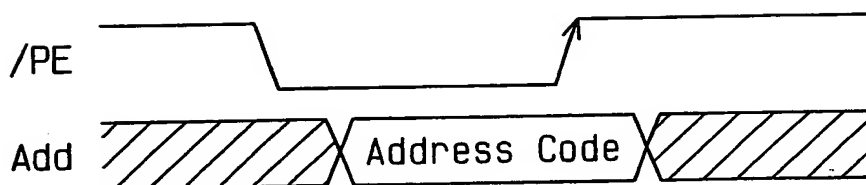


Fig.6

A1	A2	A3	Refreshing Section
H	L	L	32Mb Upper Addresses
L	H	L	16Mb Upper Addresses
H	L	H	32Mb Lower Addresses
L	H	H	16Mb Lower Addresses

Fig.7

Fuse			Refreshing Section
Fc1	Fc2	Fc3	
Broken	Not Broken	Not Broken	32Mb Upper Addresses
Not Broken	Broken	Not Broken	16Mb Upper Addresses
Broken	Not Broken	Broken	32Mb Lower Addresses
Not Broken	Broken	Broken	16Mb Lower Addresses

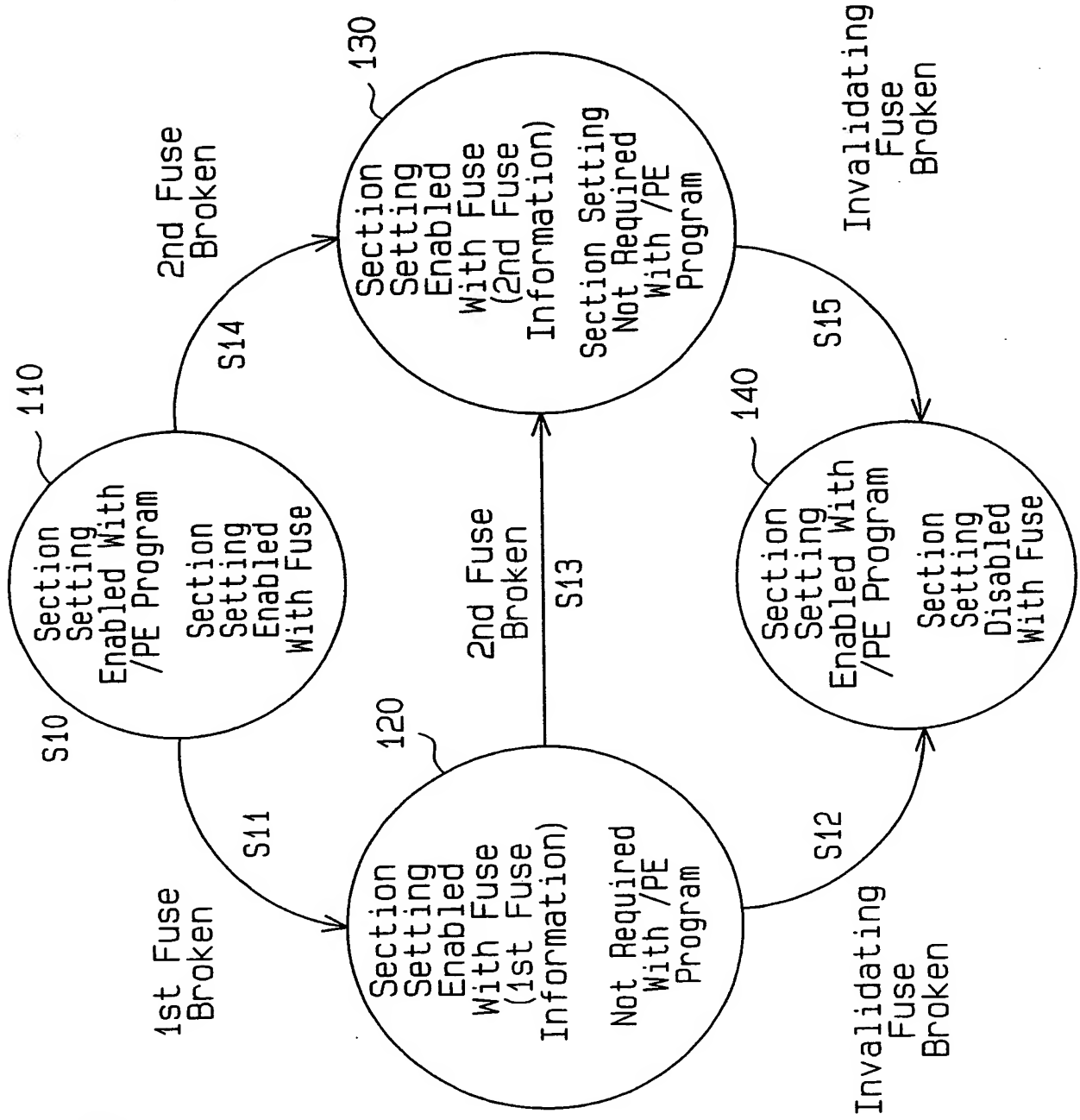


Fig.8

Fig.9

